

Exploiting the Correlation between Dependence Distance and Latency in Loop Pipelining for HLS

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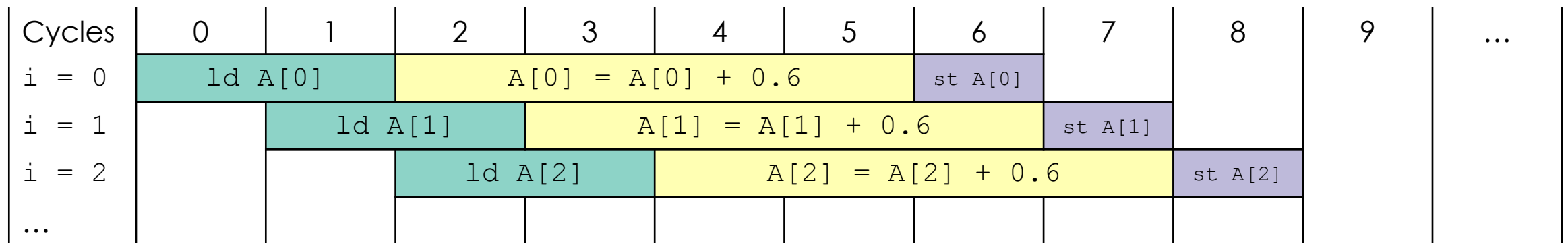
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Loop Pipelining in HLS

```
1  for (int i = 0; i < N; i++) {  
2      A[i] = A[i] + 0.6;  
3  }
```

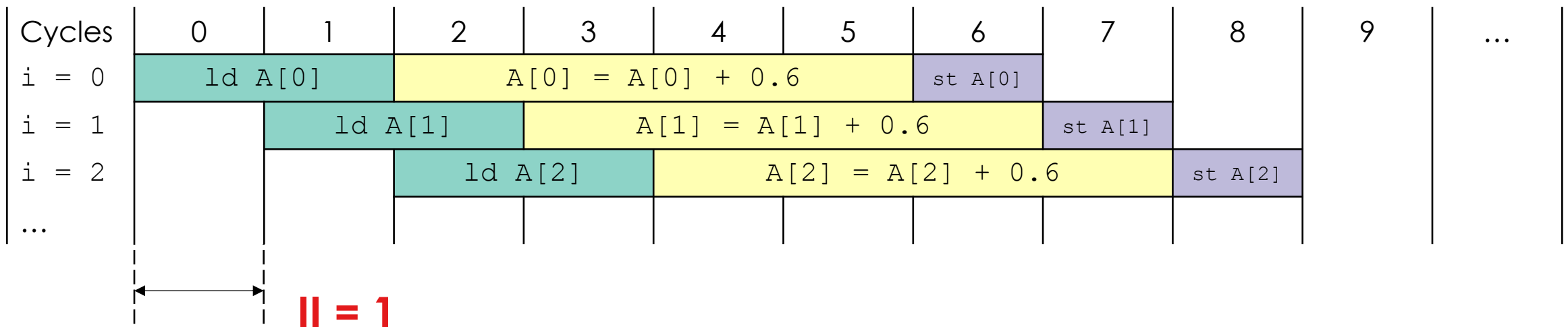
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```



Initiation Interval (II): The number of clock cycles between the start times of the same operation in two consecutive loop iterations.

Motivating example

Can Vitis HLS always find the optimal II?

```
1  for (int i = 0; i < N; i++) {
2      double e = vec[i];
3      if (e > 0)
4          vec[i+63] = (((((e+0.64)*e
5                      +0.7)*e+0.21)*e+0.33)
6                      *e+0.25)*e+0.125;
7      else
8          vec[i*i+9] = e * e;
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```

- Vitis HLS automatically finds an “optimal” **II = 41**
- By simulation, the actual optimal **II = 2**




Automatically Determining II

```
1 float s = s0;  
2 for (int i = 0; i < N; i++) {  
3     s = s + 0.6;  
4 }
```


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Dependence distance = 1

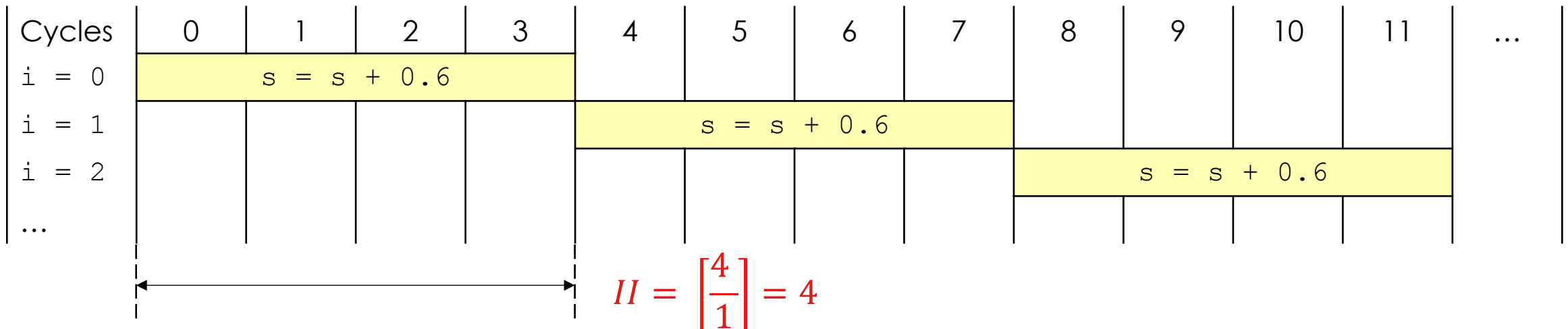
Latency of line 3 = 4 cycles

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```

Dependence distance = 1

Latency of line 3 = 4 cycles




Automatically Determining II

```
1  for (int i = 0; i < N; i++) {  
2      A[i+3] = A[i] + 0.6;  
3  }
```


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Automatically Determining II

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1  for (int i = 0; i < N; i++) {  
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Dependence distance = 3

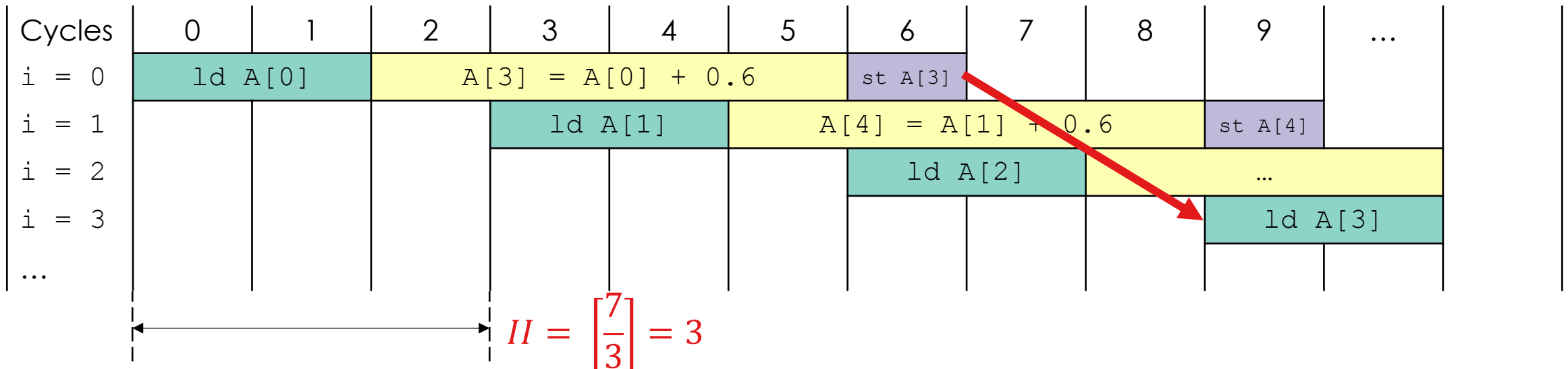
Latency of line 2 = 7 cycles

Automatically Determining II

```
1  for (int i = 0; i < N; i++) {  
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3  }
```

Dependence distance = 3

Latency of line 2 = 7 cycles



Automatically Determining II

Iteration latency: The number of clock cycles between the start times of an operation and its dependants

Dependence distance: The number of iterations that separate an operation from its dependants

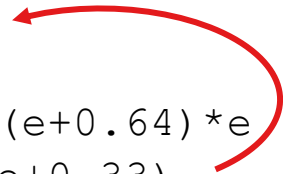
$$II = \left\lceil \frac{\textit{Iteration latency}}{\textit{Dependence distance}} \right\rceil$$

Motivating example

```
1  for (int i = 0; i < N; i++) {
2      double e = vec[i];
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5                      +0.7)*e+0.21)*e+0.33)
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```

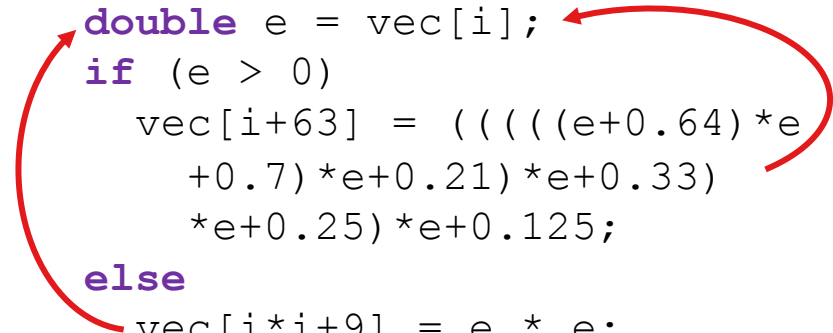

Motivating example

```
1  for (int i = 0; i < N; i++) {  
2      double e = vec[i];  
3      if (e > 0)  
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Motivating example

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Automatically Determining II

Iteration latency: The number of clock cycles between the start times of an operation and its dependants

Dependence distance: The number of iterations that separate an operation from its dependants

$$II = \left\lceil \frac{\textit{Iteration latency}}{\textit{Dependence distance}} \right\rceil$$

Automatically Determining II

Iteration latency: The number of clock cycles between the start times of an operation and its dependants

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$$II = \left\lceil \frac{\textit{Iteration latency}}{\textit{Dependence distance}} \right\rceil \quad \begin{array}{c} \text{Existing works} \\ \rightarrow \end{array} \quad II = \left\lceil \frac{\max_D \textit{Iteration latency}}{\min_D \textit{Dependence distance}} \right\rceil$$

Automatically Determining II


Iteration latency: The number of clock cycles between the start times of an operation and its dependants

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Existing works 

$$II = \left\lceil \frac{\max_D \textit{Iteration latency}}{\min_D \textit{Dependence distance}} \right\rceil$$

 Our work

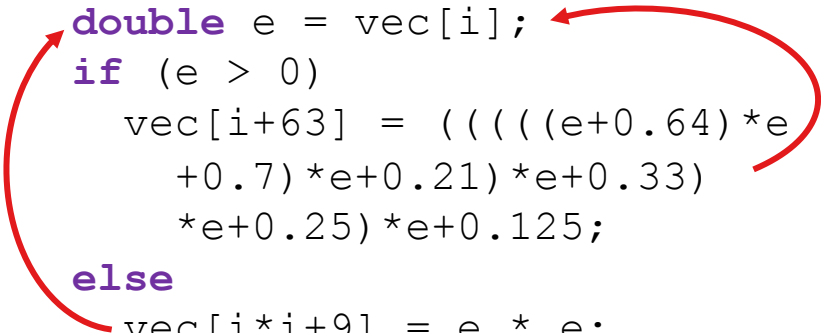
$$II = \max_D \left\lceil \frac{\textit{Iteration latency}}{\textit{Dependence distance}} \right\rceil$$

Our Contributions

- A new loop pipelining formulation that includes the correlations between the iteration latency and the dependence distance
- A SMT-based approach to support non-linear memory access analysis
- A fully automated HLS pass that finds the optimal II for a loop

Motivating example

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Latency of line 4 = 82 cycles
Dependence distance = 63

Latency of line 8 = 8 cycles
Dependence distance = 9

Motivating example

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1  for (int i = 0; i < N; i++) {  
2      double e = vec[i];  
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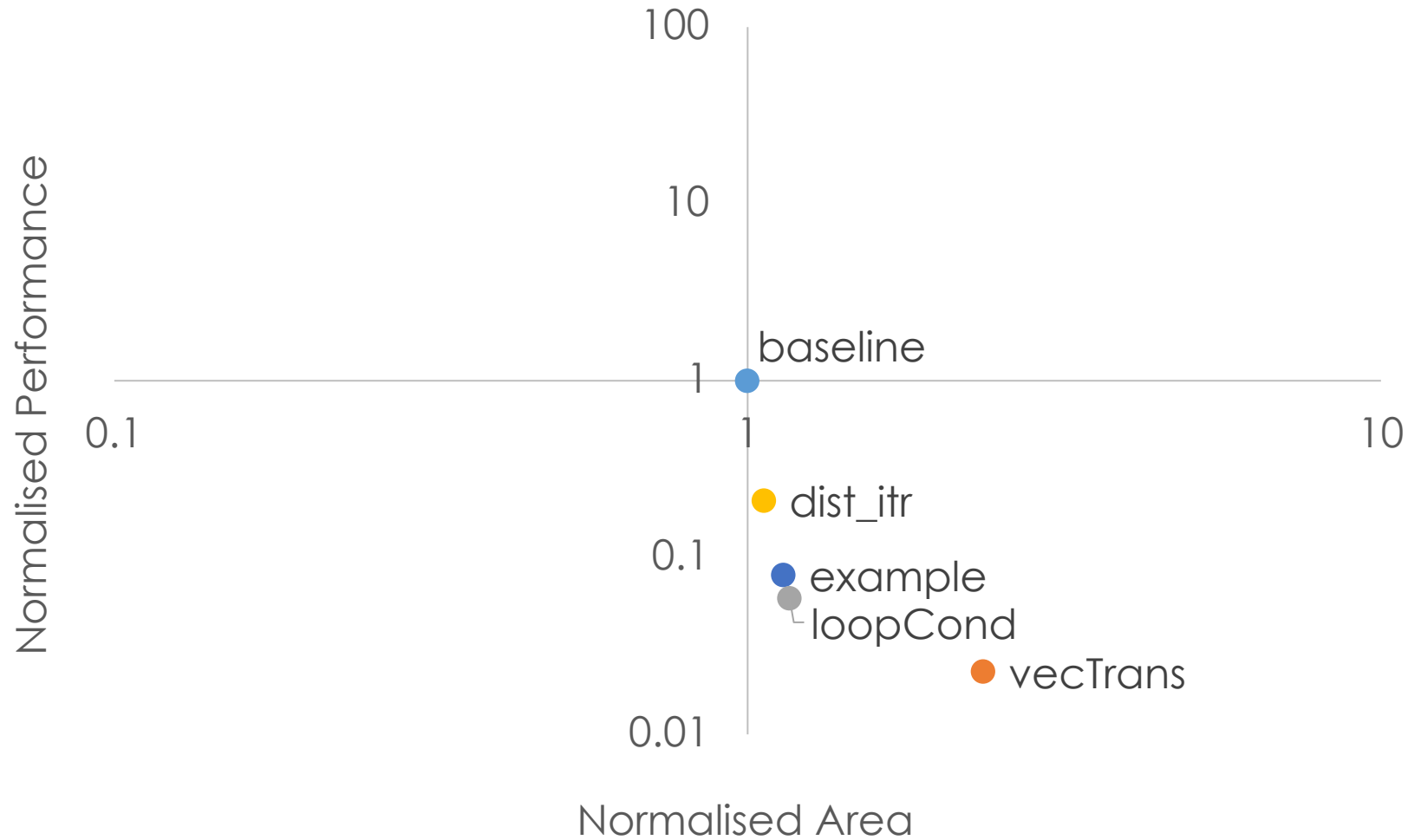
Original formulation:

$$II = \left\lceil \frac{\max\{82, 8\}}{\min\{63, 9\}} \right\rceil = \left\lceil \frac{82}{9} \right\rceil = 10$$

Our formulation:

$$II = \max \left\{ \left\lceil \frac{82}{63} \right\rceil, \left\lceil \frac{8}{9} \right\rceil \right\} = \max\{2, 1\} = 2$$

Experiments



11.1x speedup
1.95x area

Exploiting the Correlation between Dependence Distance and Latency in Loop Pipelining for HLS

COME and TALK to us!



Paper



Tool

Paper url: <https://jianyicheng.github.io/papers/ChengFPL21.pdf>

Tool url: <https://github.com/JianyiCheng/iiProver>